

Amendments to the Specification:

Please replace the first paragraph on page 1 with the following amended paragraph:

B1 | The present invention relates to an echo canceler, and more specifically to an echo canceler for canceling echo components in a local input signal of hands-free automobile telephone sets and international telephone circuit.

Please replace the third paragraph on page 5 with the following amended paragraph:

B2 | When the power of the tap coefficients $h(t)$ for the segments is above a predetermined threshold at the time for adjusting the tap coefficients, the product for the segments is computed using the 16 bits processor 112 that is accurate and requires extensive computation. When the power of the tap coefficients $h(t)$ for the segments is below the predetermined threshold at the time for adjusting the tap coefficients, the product for the segments is computed using the 8 bits processor 114 that ~~dese~~ does not require extensive computation. In this computation, the tap coefficients are 8 bits parameter. As described above, the product for the segments is assigned to the appropriate adaptive filter every segment according to the computational requirements for the segments.

2nd Full
Please replace the ~~eighth~~ paragraph on page 9 with the following amended paragraph:

B3 | That is the echo canceler creates the echo replica ER8 using 8 bits computation that ~~dese~~ does not require extensive computation. At the leading edge, 16 bits computation is used for the segment that the overflow occurs.

8h 10
Please replace the ~~third~~ paragraph on page 11 with the following amended paragraph:

B4
The echo replica ER is created~~s~~ by the adder 115 by adding the echo replica ER8 created using the 8 bits processor 114 to the echo replica ER16 created by the 16 bits processor 112 using the reassigned local input Rin because of the overflow.

7h 11
Please replace the ~~third~~ paragraph on page 12 with the following amended paragraph:

B5
The time for computation when the overflow occurs can be delayed. In order to delay, for example, an overflow storing means, not shown, is comprised, and stores the local input signal series $x(t)$ and the tap coefficients $h(t)$ for the segments that the overflow occurs in the adaptive filter 102, and computation for the last one segment is computed before present computation. Also, it is possible that the 8 bits processor 114 and the additional processor 317 are connected and compute together according to order from the controller 111. The accurate 16 bits computation as a whole system is the purpose. The time and method for computation is not limited to the embodiment.

4th Full 12
Please replace the ~~second~~ paragraph on page 13 with the following amended paragraph:

B4
The above ~~three~~ of many possible embodiments of the present invention uses the transfer characteristics of the echo path of the 8 bits processor 114 and the 16 bits processor 112 as decision means. The decision means is not limited to the transfer characteristics of the echo path. It is possible to use the means that can estimate computational requirement of the adaptive filter 102.